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FACSIMILE COVER SHEET

TO: EXAMINER D. WILLE (U.S. PATENT AND TRADEMARK OFFICE)

CLIENT NUMBER: 53527

2814

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FROM: MICHAEL W. TAYLOR, ESO.

DATE: September 5, 2002

NUMBER OF PAGES (INCLUDING COVER SHEET): 18

COMMENTS/INSTRUCTIONS:

Re: U.S. Patent Application Serial No. 09/653,390

Attached are copies of the following documents.

1. Amendment Transmittal
2. Amendment (Official Action of 3/5/02)
3. Request for Extension of Time

NOTE: The information in this facsimile transmission is intended only for the personal and confidential use of the designated recipient(s) named above. This message may be an attorney-client communication and as such is privileged.

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TRANSMISSION, PLEASE CONTACT THE RECEPTIONIST IMMEDIATELY AT (407) 841-2330.**

Case No. 99CT22053527DIRECTOR, U.S. PATENT AND TRADEMARK OFFICE
WASHINGTON, DC 20231In re Application of: **COFFA ET AL.**Serial No.: **09/653,390**Filed: **September 1, 2000**For: **SEMICONDUCTOR DEVICE FOR ELECTRO-OPTIC APPLICATIONS, METHOD
FOR MANUFACTURING SAID DEVICE AND CORRESPONDING
SEMICONDUCTOR LASER DEVICE**

Sir:

Transmitted herewith is an amendment in the above-identified application.

Small entity status of this application under 37 CFR § 1.27 has been established by a verified statement previously submitted. **FAX/COPY RECEIVED**

A verified statement to establish small entity status under 37 CFR § 1.27 is enclosed. **SEP 5 - 2002**

No additional fee is required.

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The fee has been calculated as shown below:

	(Col. 1)	(Col. 2)	(Col. 3)		SMALL ENTITY			LARGE ENTITY	
FOR:	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA		RATE	Fee		RATE	Fee
TOTAL CLAIMS		20			X 9	\$		X 18	\$
INDEPT CLAIMS		3			X 42	\$		X 84	\$
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEP. CLAIM					+ 135	\$		+ 270	
					TOTAL ADD'L FEE			TOTAL ADD'L FEE	

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 ** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, write "20" in this space.
 *** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, write "3" in this space. The "Highest Number Previously Paid For" (Total or Independent) is the highest number found from the equivalent box in Col. 1 of a prior amendment of the number of claims originally filed.

Enclosed is our check in the amount of \$. The Commissioner is hereby authorized to charge or credit any discrepancies in fee amounts to Deposit Account No. 01-0484.

The Commissioner is hereby authorized to charge or credit any discrepancies in fee amounts to Deposit Account No. 01-0484.

PLEASE ADDRESS ALL CORRESPONDENCE TO ATTORNEY OF RECORD: CHRISTOPHER F. REGAN

Please associate this application with Customer No. 27975.



27975

PATENT TRADEMARK OFFICE

MICHAEL W. TAYLOR
REG. NO. 43,182

September 5, 2002
DATE

In re Patent Application of:
COFFA ET AL.
Serial No. 09/653,390
Filing Date: **September 1, 2000**

CERTIFICATE OF FACSIMILE TRANSMISSION

I HEREBY CERTIFY that the foregoing correspondence has been forwarded via facsimile number 703-308-7722 to the Director, U.S. Patent and Trademark Office, Washington, D.C. 20231 this 5 day of September, 2002.

Kirsten Danner

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#11B Andt: det
M. Johnson
9/7/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
COFFA ET AL.)
Serial No. 09/653,390)
Filing Date: September 1, 2000)
For: SEMICONDUCTOR DEVICE FOR)
ELECTRO-OPTIC APPLICATIONS,)
METHOD FOR MANUFACTURING SAID)
DEVICE AND CORRESPONDING)
SEMICONDUCTOR LASER DEVICE)

Examiner: D. Wille

Art Unit: 2814

Fax No. 703-308-7722

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AMENDMENT

SEP 5 - 2002

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Director, U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Responsive to the Official Action of March 5, 2002,
please enter the amendments and remarks set out below.

In the Specification:

Please replace the paragraph beginning at page 5,
line 33 with the following rewritten paragraph:

-- The manufacturing process will now be described step by step. A silicon-on-insulator (SOI) wafer is provided as a substrate 2 for the semiconductor device 1. A known SIMOX or BESOI technology may be used to provide the SOI wafer. However, instead of using an SOI wafer, the substrate for the semiconductor device 1 may simply comprise a double layer of a semiconductor material. For instance, the double layer may include a first highly doped substrate layer and a

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